

STMicroelectronics LIG University of Grenoble



# Programming-Model Centric Debugging for Multicore Embedded Systems

Kevin Pouget, UJF-LIG, STMicroelectronics Miguel Santana, STMicroelectronics Jean-François Méhaut, UJF-CEA/LIG

MAD Workshop'14, Athens, Greece - October 8th, 2014



#### **Consumer Electronics Devices**

- 4K digital televisions
- Smartphones
- Hand-held music players
- High-resolution multimedia apps
  - H.265 HEVC
  - Augmented reality
  - 3D video games
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#### $\Rightarrow$ high performance expectations.

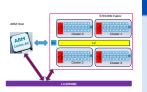


Current applications have high performance expectations...



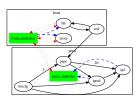
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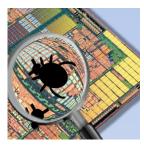
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  - Programming models & environments
- Efficient verification & validation tools
  - Workshop and our research effort

Agenda

**2** Programming Model Centric Interactive Debugging

**3** Model-Centric Debugger Case-Study

Agenda

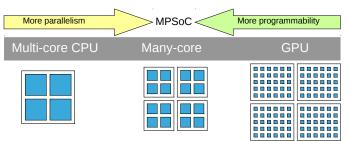
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8 Model-Centric Debugger Case-Study

Slide 3 — Kevin Pouget — Programming-Model Centric Debugging — MAD Workshop'14, Athens, Greece — October 8th, 2014

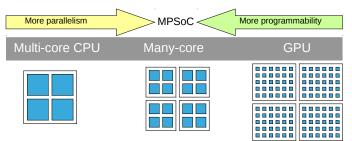
# Background: MPSoC Programming and Debugging MPSoC and GPU Systems

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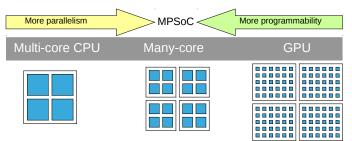


- Many-core processor for embedded systems
- Low energy-consumption
- Heterogeneous computing power



# Background: MPSoC Programming and Debugging MPSoC and GPU Systems

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#### How to program such complex architectures?

Programming Models and Supportive Environments

... with programming models!

- Programmability with high-level abstractions
- **Portability** thanks to an hardware-independent interface
- Separation of concerns between application / lower levels

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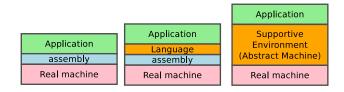
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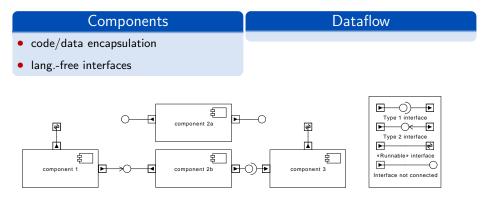
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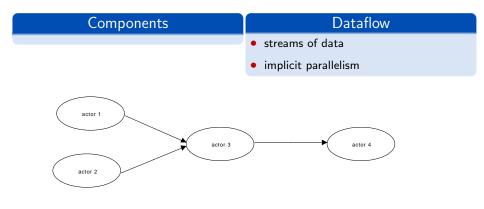
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Components	Dataflow
code/data encapsulation	• streams of data
langfree interfaces	• implicit parallelism

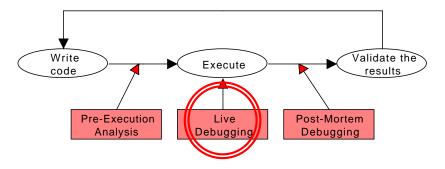
large programming domain coverage...

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large programming domain coverage...

... but what about Verification & Validation of MPSoC applications?

Tools and Techniques, Advantages of Interactive Debugging



#### Interactive Debugging (eg.: GDB)

- Developers mental representation VS. actual execution
- Understand the different steps of the execution

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- Step-by-step execution
- Memory and processor inspection

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 $\Rightarrow$  Debuggers cannot access the *abstract* machine!

#### Objective

Provide developers with means to better understand the state of the high-level applications and control more easily their execution, suitable for various models and environments.

Agenda

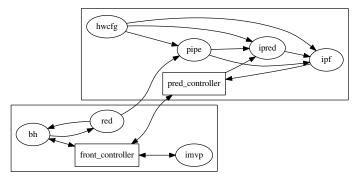
#### **2** Programming Model Centric Interactive Debugging

#### 8 Model-Centric Debugger Case-Study

#### Idea: Integrate programming model concepts in interactive debugging

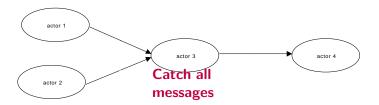
#### 1 Provide a Structural Representation

- Draw application architecture diagrams
- · Represent the relationship between the entities
- Offer catchpoints on architecture-related operations

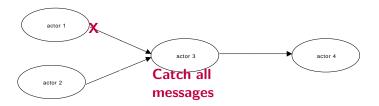


Dataflow graph from the case-study

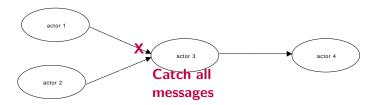
- Monitor the collaboration between the tasks
- Detect communication, synchronization events
  - interpret their pattern and semantics (one-to-one, one-to-many, global or local barriers)
- Offer communication-aware catchpoint mechanisms



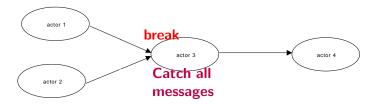
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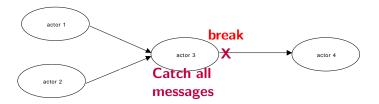
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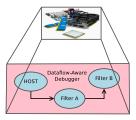


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#### 3 Interact with the Abstract Machine

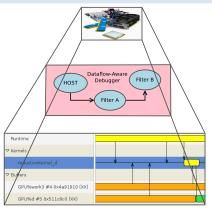
- Recognize the different entities of the model
- Provide details about their state, schedulability, callstack, ...
- Provide support to understand how they reached their current state



# Programming Model Centric Interactive Debugging

### 3 Interact with the Abstract Machine

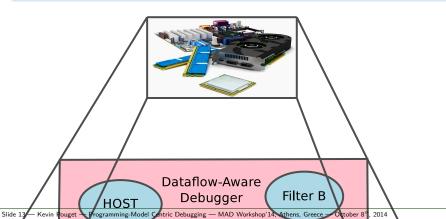
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# Programming Model Centric Interactive Debugging

### **3** Interact with the Abstract Machine

- Support interactions with real machine
  - memory inspection
  - breakpoints
  - step-by-step



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1 Background: MPSoC Programming and Debugging

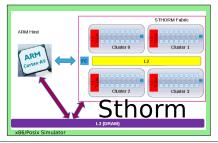
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Proof-of-concept Environment

# STHORM / Platform 2012 ST/CEA MPSoC research platform

x86 platform simulators



Proof-of-concept Environment

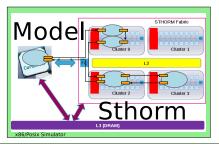
### STHORM Progr. Environments

- Dataflow (PEDF)
- Components (NPM)
- Kernels (OpenCL)

STHORM / Platform 2012

ST/CEA MPSoC research platform

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Proof-of-concept Environment

### The GNU Debugger

- Adapted to low level/C debugging
- Large user community

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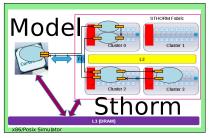
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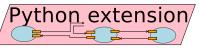
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- Large user community
- Extendable with Python API

STHORM Progr. Environments

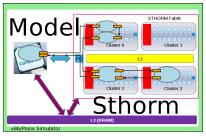
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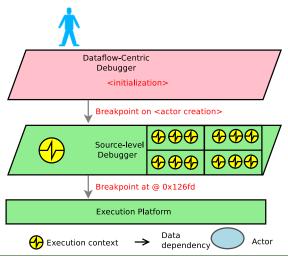


Interpreting Execution Events

 $\Rightarrow$  Detect and interpret the exec. events of the runtime framework

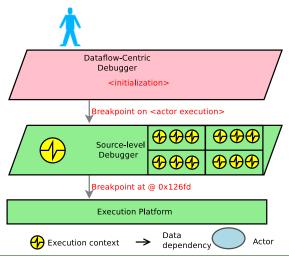
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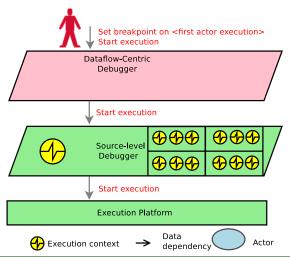
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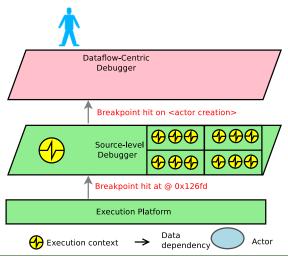
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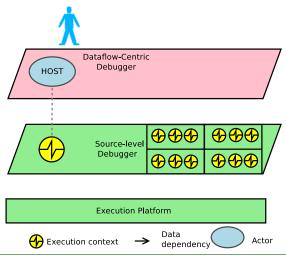
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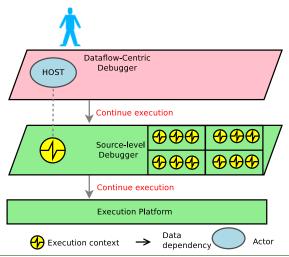
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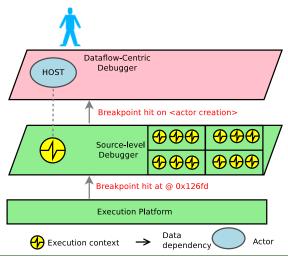
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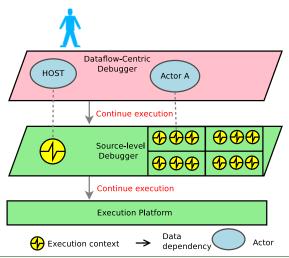
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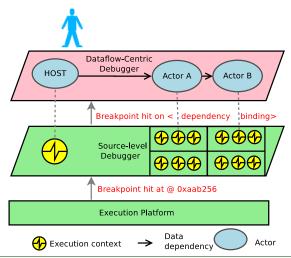
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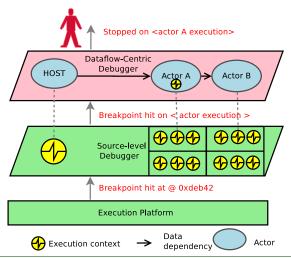
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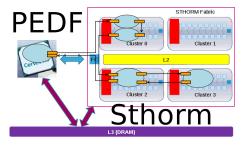


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# Model-Centric Debugger Case-Study

Dataflow Video Decoder

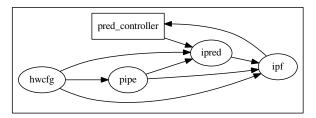




# Model-Centric Debugger Case-Study: Dataflow Video Decoder

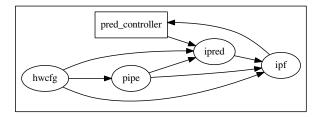
The application is frozen, how can GDB help us?

hint: not much!



(static graph provided by the compiler)

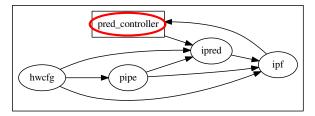
# Model-Centric Debugger Case-Study: Dataflow Video Decoder The application is frozen, how can GDB help us?



### (gdb) info threads

Id	Target Id	Frame
1	Thread Oxf7e77b	<pre>0xf7ffd430 inkernel_vsyscall ()</pre>
* 2	Thread 0xf7e797	operator= (val=, this=0xa0a1330)

# Model-Centric Debugger Case-Study: Dataflow Video Decoder The application is frozen, how can GDB help us?

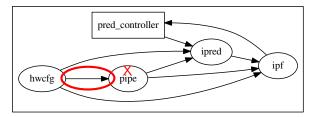


### (gdb) thread apply all where

Thread 1 (Thread Oxf7e77b):

- #0 0xf7ffd430 in \_\_kernel\_vsyscall ()
- #1 0xf7fcd18c in pthread\_cond\_wait@ ()
- #2 0x0809748f in wait\_for\_step\_completion(struct... \*)
- #3 0x0809596e in pred\_controller\_work\_function()
- #4 0x08095cbc in entry(int, char\*\*) ()
- #5 0x0809740a in host\_launcher\_entry\_point ()

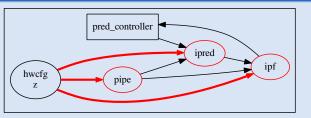
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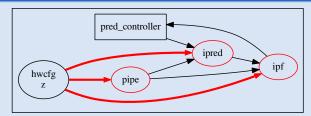
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### (mcgdb) info graph



# Model-Centric Debugger Case-Study: Dataflow Video Decoder The application is frozen, how can mcGDB help us?

### (mcgdb) info graph



### (mcgdb) info actors +state

```
#0 Controller 'pred_controller':
    Blocked, waiting for step completion
#1/2/3 Actor 'pipe/ipref/ipf':
    Blocked, reading from #4 'hwcfg'
#4 Actor 'hwcfg':
    Asleep, Step completed
```

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#### **Conclusions and Future Work**

- Debugging high-level applications is challenging
- Lack of information about programming models and frameworks

Our contribution: model-centric interactive debugging, applied to

• Component, dataflow and kernel (GPU) programming

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 $Proof-of-concept: \ {\rm MCGDB}, \ a \ prototype \ for \ {\rm STHORM} \ platform$ 

- Extends GDB and its Python interface:
  - Interface patches contributed to the community
- Usage studied through embedded and scientific applications

Perspectives for programming-model centric debugging:

- Industrial side
  - Strengthen the implementation
  - Conduct extensive impact studies
- Research side
  - Apply to different programming models
  - · Visualization-assisted interactive debugging
  - Enrich debugging information generated by compilers
    - work funded by NANO 2017 R&D project



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# Publications



Kevin Pouget.

**Programming-Model Centric Debugging for Multicore Embedded Systems.** PhD thesis, Université de Grenoble, École Doctorale MSTII, feb 2014.



Kevin Pouget, Marc Pérache, Patrick Carribault, and Hervé Jourdren. User level DB: a debugging API for user-level thread libraries. In *Parallel Distributed Processing, Workshops and Phd Forum (IPDPSW), 2010 IEEE International Symposium on*, pages 1–7, 2010.

Kevin Pouget, Miguel Santana, Vania Marangozova-Martin, and Jean-François Mehaut. Debugging Component-Based Embedded Applications. In *Joint Workshop Map2MPSoC* (*Mapping of Applications to MPSoCs*) and SCOPES (Software and Compilers for Embedded Systems), St Goar, Germany, may 2012. Published in the ACM library.



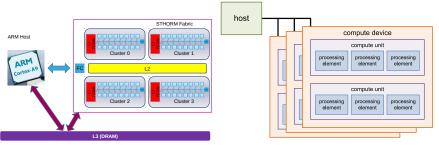
Kevin Pouget, Patricia López Cueva, Miguel Santana, and Jean-François Méhaut. Interactive Debugging of Dynamic Dataflow Embedded Applications. In *Proceedings of the 18th International Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS)*, Boston, Massachusetts, USA, may 2013. Held in conjunction of IPDPS.

Kevin Pouget, Patricia López Cueva, Miguel Santana, and Jean-François Méhaut. A novel approach for interactive debugging of dynamic dataflow embedded applications. In *Proceedings of the 28th Symposium On Applied Computing (SAC)*, pages 1547–1549, Coimbra, Portugal, apr 2013.

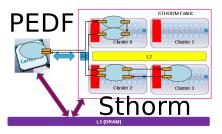
Interact with the Abstract Machine

### ST STHORM Platform — our reference MPSoC

- CPU + 4 clusters × 16 lightweight/energy-efficient cores
  - $\pm$  dedicated hardware accelerators
- GPU-like architecture



#### Dataflow Video Decoder





- Dynamic dataflow programming
- Good for multimedia application
- No verification/validation help
- Heterogeneous computing:
  - actors  $\Rightarrow$  HW accelerators



- Flexible video decoding standard
  - for HD television, blu-ray disks, broadcast, telephony, ...
- Good dataflow decomposition
- Developed to validate PEDF design